

Docket No.: G0631.70038US00

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

James Anthony Power et al.

Serial No.:

10/721,039

Confirmation No.:

5026

Filed: For:

November 21, 2003

INTEGRATED MOS ONE-WAY ISOLATION COUPLER, A METHOD FOR ONE-WAY COUPLING AN INPUT SIGNAL TO AN

INTEGRATED CIRCUIT, AND A SEMICONDUCTOR CHIP HAVING AN INTEGRATED MOS ISOLATION ONE-WAY

COUPLER LOCATED THEREON

Examiner:

Long Pham

Art Unit:

2814

Certificate of Mailing Under 37 CFR 1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 20, 2006

Steven J. Henry, Registration No. 27,900

Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

## RESPONSE TO RESTRICTION AND ELECTION REQUIREMENT

Applicant is in receipt of the communication mailed November 2, 2006. This communication contains (1) a restriction requirement and (2) an election requirement. In response, Applicant provisionally elects (with traversal) product claims 1-32, and the species 1 claims thereof, being claims 1-11 and 17-26.